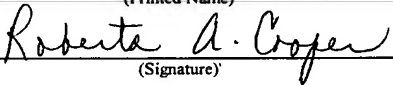




Atty. Dkt. No. 039153-5003 (G0167)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Xiang, Qi
Title: SILICON BUFFERED SHALLOW
TRENCH ISOLATION
(formerly "Silicon Buffered
Shallow Trench Isolation For
Strained Silicon Processes")
Appl. No.: 10/755,746
Filing Date: 01/12/2004
Examiner: To be determined
Art Unit: 2813

CERTIFICATE OF EXPRESS MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
EV 459170495 US (Express Mail Label Number)	01/12/05 (Date of Deposit)
Roberta A. Cooper (Printed Name)	
 (Signature)	

SUPPLEMENTAL PRELIMINARY AMENDMENT UNDER 37 CFR 1.115

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Prior to examination of the present Application, Applicant respectfully requests that the application be amended as follows:

Amendments to the Title are shown in page 2 of this document.

Amendments to the Specification begin on page 3 of this document.

Amendments to the Claims begin on page 4 of this document.

Remarks begin on page 7 of this document.